

**Institute of Microelectronics of Barcelona IMB-CNM (CSIC)**

## **Design of a Digital AES-256 Encoder/Decoder**

The IMB-CNM is the largest institute in Spain dedicated to the research and development of Micro and Nano Technology and microsystems and with unique capabilities in silicon technology. It belongs to CSIC since its foundation in 1985 and is distinguished with the AEI María de Maeztu Unit of Excellence accreditation.

The main activities of IMB-CNM are basic and applied research and development, education and training in micro and nanotechnologies, components and systems. Its mission is to improve knowledge and contribute to the implementation of solutions based on these technologies in new products to solve societal challenges.

This work will consist on the design of a Digital AES-256 Encoder/Decoder including the RTL design, Validation and Verification and Place and Route of the design

### Key Responsibilities

- Research of the state of the art
- Design and simulation of the different blocks

### Required skills

- Previous knowledge on CMOS technologies/design
- Knowledge Verilog, SystemVerilog and/or VHDL

### Experience

- Having coursed the Microelectronic design subject

### Job conditions

- Flexible hours
- TFG/TFM or work placemanship

### How to apply

Applications should be submitted to [rrhh@imb-cnm.csic.es](mailto:rrhh@imb-cnm.csic.es) and [miquel.redon@imb-cnm.csic.es](mailto:miquel.redon@imb-cnm.csic.es) stating the job title as the Subject of the message. The mail should include a cover letter and an updated CV.

Sending CVs to the above address implies consent to the Legal Advice | IMB CNM (csic.es).

This offer can be found on:

<https://www.imb-cnm.csic.es/en/about-center/careers/open-positions>.

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